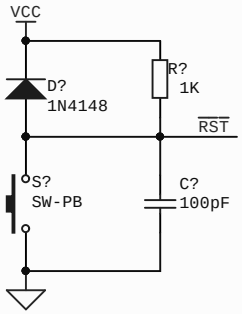
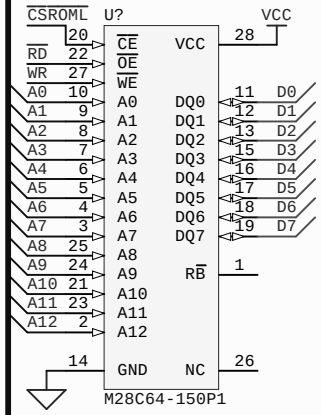
## Reset Circuit



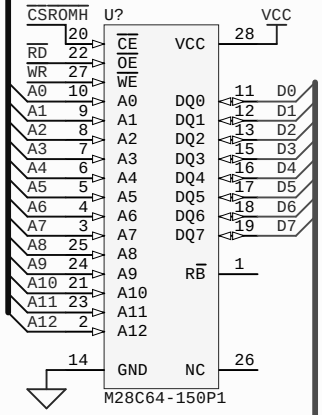

## CPU



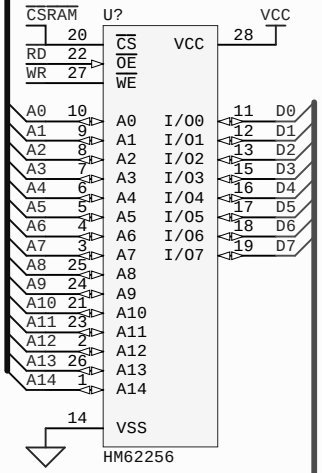

## ROM



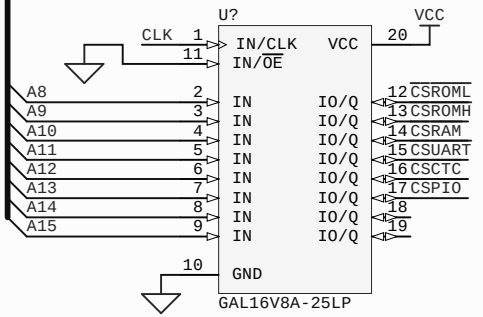

## ROM



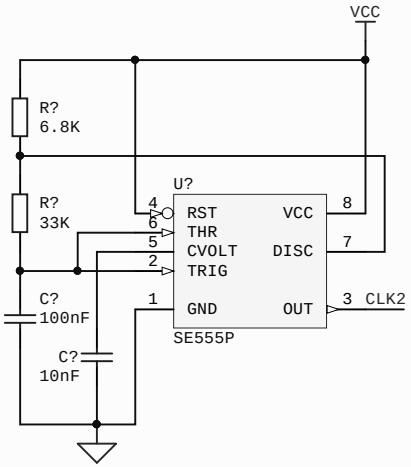

## RAM




## Address Decoder




## Clock Circuits

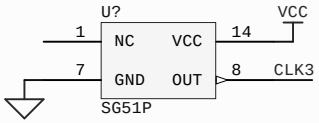


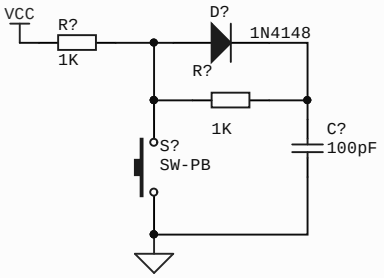

## Address Space Map

| |
|---|
| 8KB ROM |
| 8KB ROM |
| 16KB I/O SPACE |
| 32KB RAM |

Z80 Micro Computer

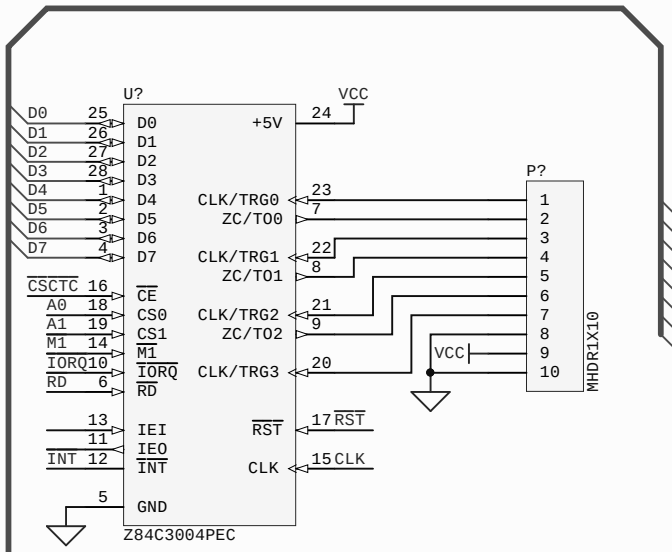
Per il progetto di semestre al corso di elettronica presso la Scuola Arti e Mestieri di Bellinzona ho scelto di realizzare un computer con un processore Zilog Z80 alla sua base. Lo scopo del progetto e` di meglio comprendere la struttura elettronica e informatica di questa invenzione rivoluzionaria.



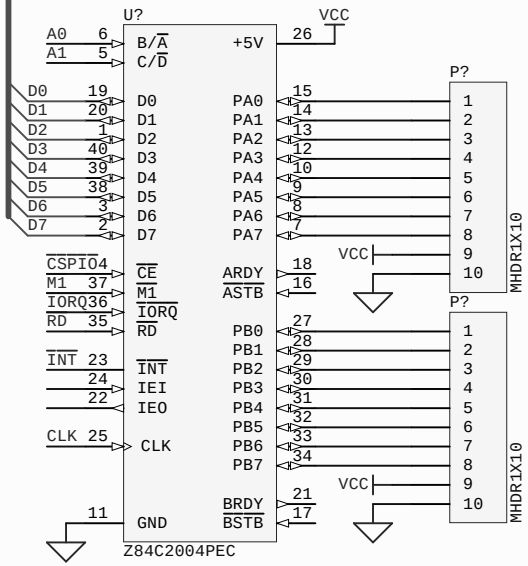




| | | |
|---|---|---|
| Title: z80 Single Board Computer: Base | | |
| Size: A4 | Number: | Revision: |
| Date: 20.03.2017 | | Sheet 1 of 4 |
| File: F:\School\..\MainSheet.SchDoc | | Drawn By: Naoki Pross |

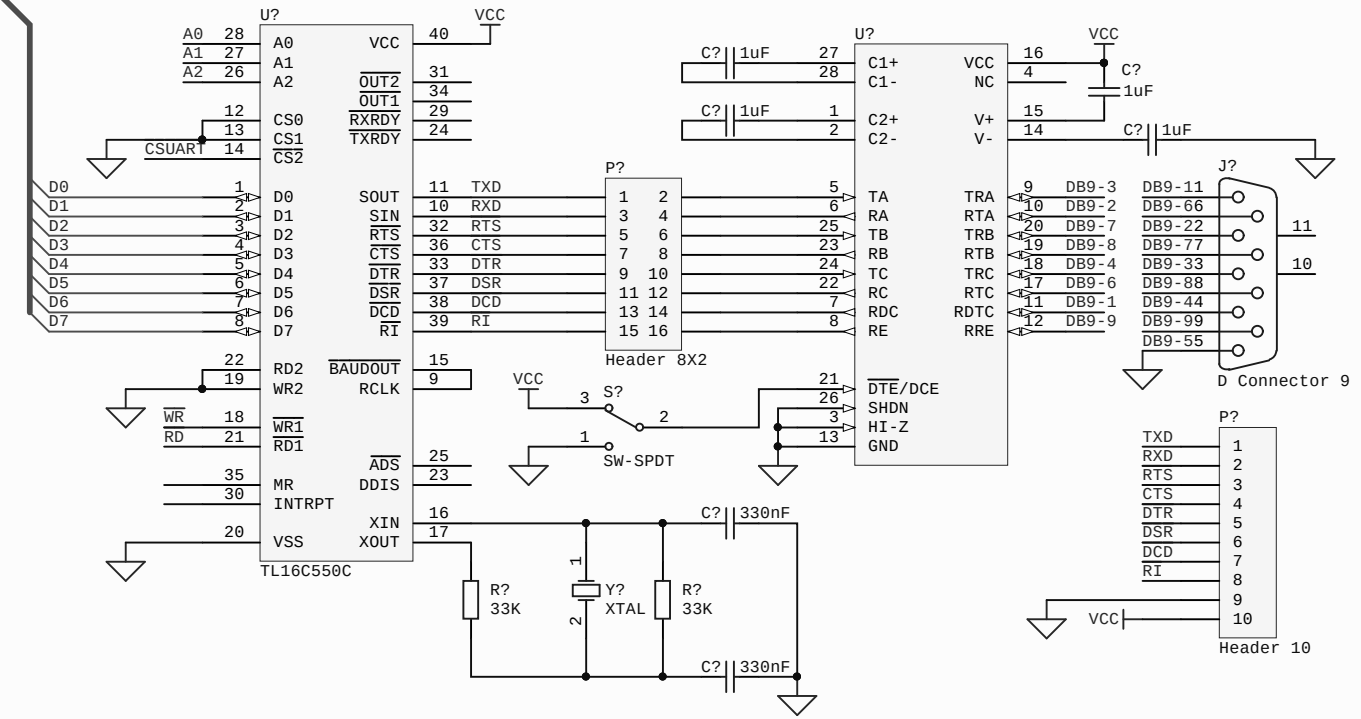
## Counter Timer Circuit



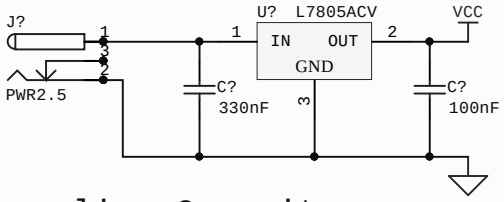

## Parallel I/O Controller



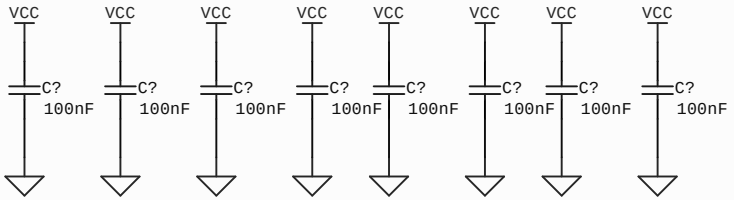

## Serial Communication Device



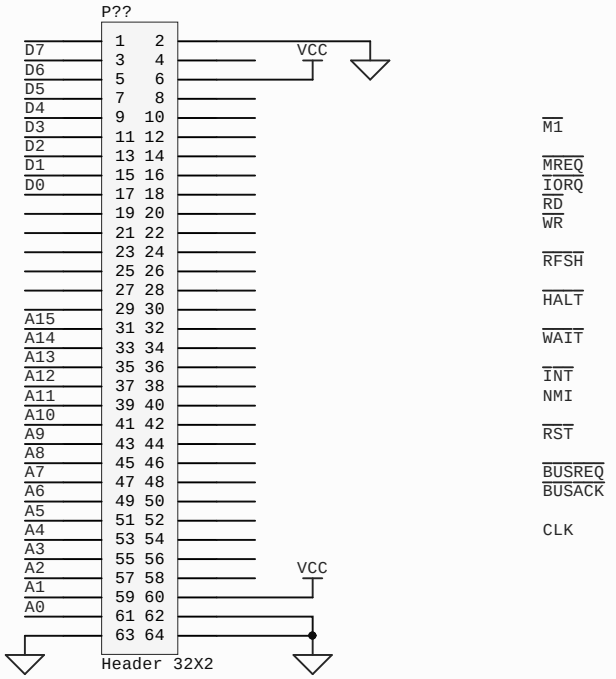

| | | |
|---|---|---|
| Title: z80 Single Board Computer: I/O Devices | | |
| Size: A4 | Number: | Revision: |
| Date: 20.03.2017 | | Sheet 2 of 4 |
| File: F:\School\..\IODevices.SchDoc | | Drawn By: Naoki Pross |



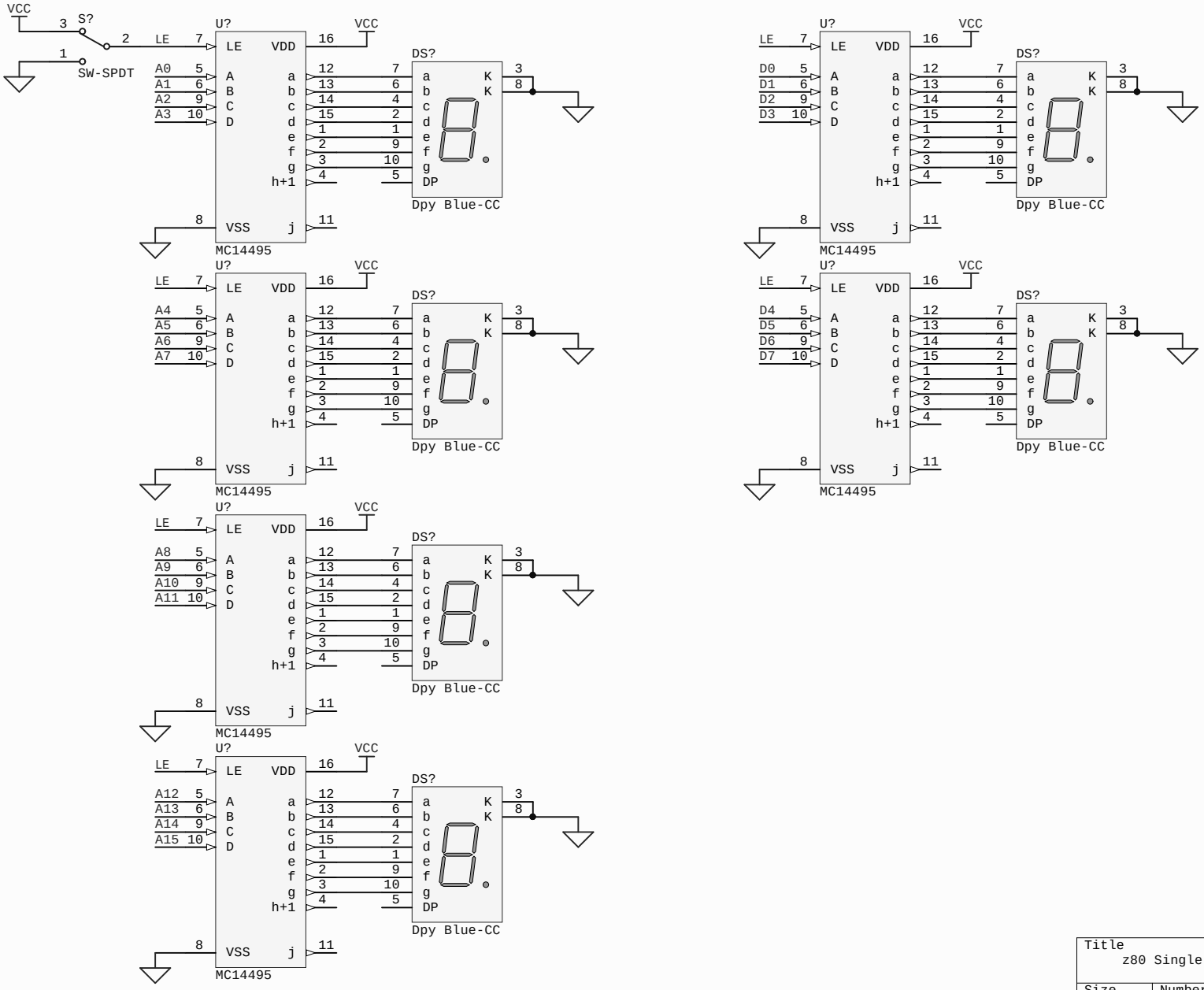

## Decoupling Capacitors




## PC/104 Connector




| | | |
|---|---|---|
| Title: z80 Single Board Computer: Power Supply Circuits | | |
| Size: A4 | Number: | Revision: |
| Date: 20.03.2017 | | Sheet 3 of 4 |
| File: F:\School\..\Power.SchDoc | | Drawn By: Naoki Pross |




| | | |
|---|---|---|
| Title: z80 Single Board Computer: Bus Data Visualizer | | |
| Size: A4 | Number: | Revision: |
| Date: 20.03.2017 | Sheet 4 of 4 | |
| File: F:\School\..\BusViewer.SchDoc | Drawn By: Naoki Pross | |